

MZVPW128HEGM-00000
MZVPW256HEGL-00000
MZVKW512HMJP-00000
MZVKW1T0HMLH-00000

Target

M.2 NVMe PCIe SSD specification

(SM961)

datasheet

SAMSUNG ELECTRONICS RESERVES THE RIGHT TO CHANGE PRODUCTS, INFORMATION AND SPECIFICATIONS WITHOUT NOTICE.

Products and specifications discussed herein are for reference purposes only. All information discussed herein is provided on an "AS IS" basis, without warranties of any kind.

This document and all information discussed herein remain the sole and exclusive property of Samsung Electronics. No license of any patent, copyright, mask work, trademark or any other intellectual property right is granted by one party to the other party under this document, by implication, estoppel or otherwise.

Samsung products are not intended for use in life support, critical care, medical, safety equipment, or similar applications where product failure could result in loss of life or personal or physical harm, or any military or defense application, or any governmental procurement to which special terms or provisions may apply.

For updates or additional information about Samsung products, contact your nearest Samsung office.

All brand names, trademarks and registered trademarks belong to their respective owners.

© 2016 Samsung Electronics Co., Ltd. All rights reserved.

Revision History

<u>Revision No.</u>	<u>History</u>	<u>Draft Date</u>	<u>Remark</u>	<u>Edited by</u>	<u>Reviewed by</u>
0.1	1. Initial issue	Apr 15, 2016	Target	Brian Chae	Jenny Kang

SM961 Series

PART NUMBER	MZVPW128HEGM-00000	MZVPW256HEGL-00000	MZVKW512HMJP-00000	MZVKW1T0HMLH-00000
Capacity ¹⁾	128GB	256GB	512GB	1TB
LBA ²⁾	250,069,680	500,118,192	1,000,215,216	2,000,409,264

FEATURES (TBD)	Environmental Specifications (TBD)
<ul style="list-style-type: none"> • PCIe Gen3 8Gb/s Interface, up to 4 Lanes • Compliant with PCI Express Base Specification Rev. 3.0 • Compliant with PCI Express CEM Specification Rev. 3.0 • Partially Compliant with NVMe Express specification Rev. 1.2 • Power Saving Modes: <ul style="list-style-type: none"> - Supporting APST - Supporting L1.2 Mode • Support Admin & NVM Command Set • RoHS Compliant 	<p>Temperature</p> <p>Operating⁴⁾ 0°C to 70°C</p> <p>Non-operating -40°C to 85°C</p> <p>Humidity (non-condensing)</p> <p>Non-operating 5 ~ 95%</p> <p>Linear Shock (0.5ms duration with 1/2 sine wave)</p> <p>Non-operating 1,500 G</p> <p>Vibration</p> <p>Non-operating (10 ~ 2,000 Hz, Sinusoidal) 20 G</p>

Drive Configuration (TBD)	POWER REQUIREMENTS (TBD)
Capacity 128/256/512GB/1TB	Supply Voltage / Tolerance +3.3V ± 5%
Form Factor M.2	Voltage Ripple/Noise (max.) 100mV p-p
Interface PCI Express Gen3 x4	Active ⁵⁾ (Typ, RMS)
Bytes per Sector 512 Bytes	- Read 5.9 W
	- Write 5.7 W
	Idle ⁶⁾ (Typ.) TBD
	L1.2 ⁷⁾ (Typ) 2.5 mW

Performance Specifications ³⁾ (TBD)
Data Transfer Rate (128KB)
Sequential Read (1TB/512GB) Up to 3200 MB/s
(256/128GB) Up to 3100 MB/s
Sequential Write (1TB) Up to 1800 MB/s
(512GB) Up to 1700 MB/s
(256GB) Up to 1400 MB/s
(128GB) Up to 700 MB/s
Data I/O Speed (4KB)
Random Read (1TB) Up to 450K IOPS
(512/256/128GB) Up to 330K IOPS
Random Write (1TB) Up to 320K IOPS
(512GB) Up to 300K IOPS
(256GB) Up to 280K IOPS
(128GB) Up to 170K IOPS

PHYSICAL DIMENSION (TBD)
Width 22.00 ± 0.15 mm
Length 80.00 ± 0.15 mm
Height (Single Side) Max. 2.38 mm
Weight Up to 8 g

Reliability Specifications (TBD)
UBER < 1 sector per 10 ¹⁵ bits read
MTBF 1.5 Million Hours

Specifications are subject to change without notice.

- 1) 1MB = 1,000,000 Bytes, 1GB = 1,000,000,000 Bytes, Unformatted Capacity. User accessible capacity may vary depending on operating environment and formatting.
- 2) 1 Sector = 512Bytes, Max. LBA represents the total user addressable sectors in LBA mode and calculated by IDEMA rule
- 3) Actual performance may vary depending on use conditions and environment.
- 4) Measured by SMART Temperature. Proper airflow recommended
- 5) Active power is measured on sequential write and read.
- 6) Idle Power is measured on Idle status with L1.

IF THERE IS ANY OTHER OPERATION TO IMPLEMENT IN ADDITION TO SPECIFICATION IN THE DATASHEET OR JEDEC STANDARD, PLEASE CONTACT EACH BRANCH OFFICE OR HEADQUARTERS OF SAMSUNG ELECTRONICS.